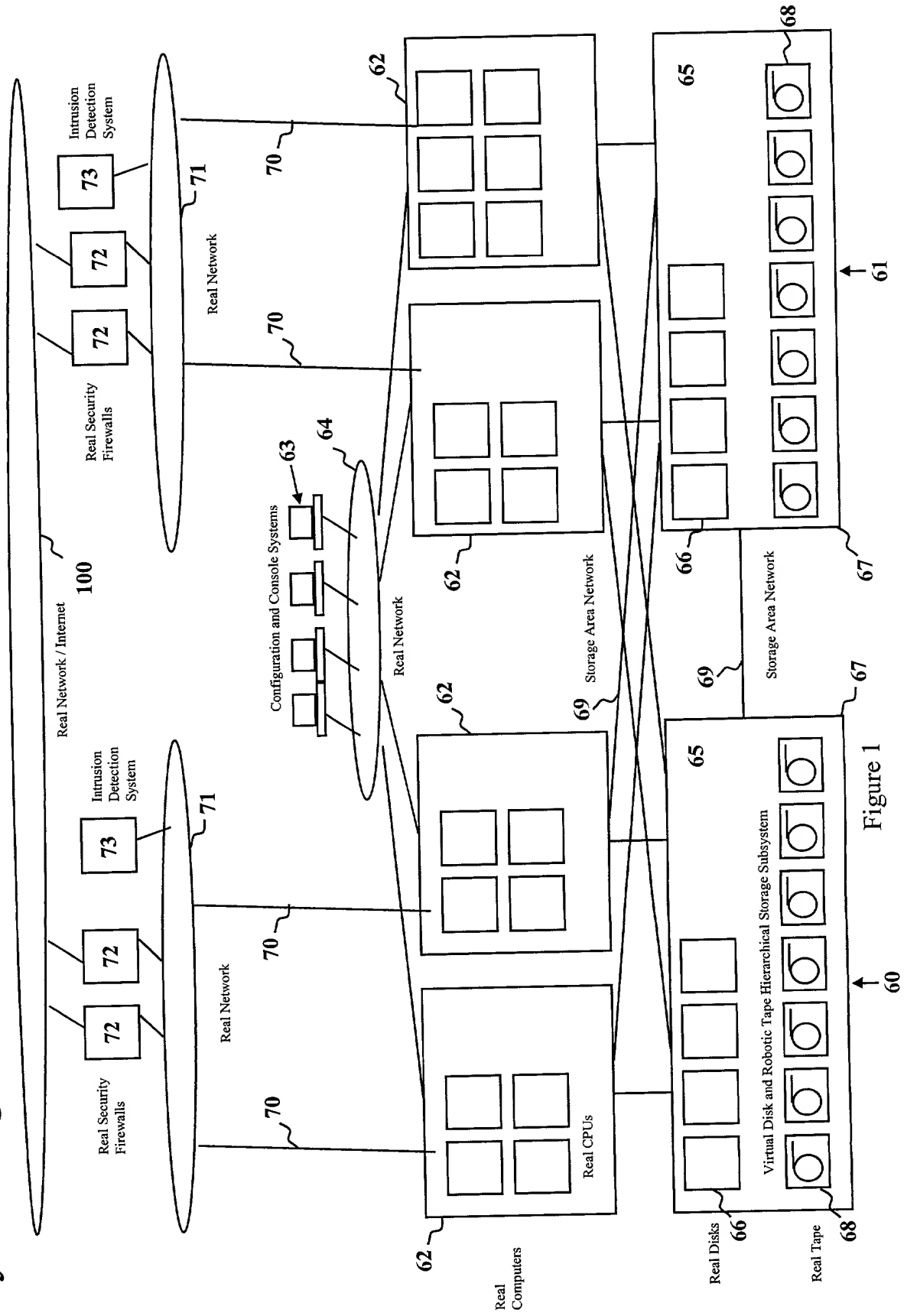


# Physical High-level System Architecture



# Logical System Architecture

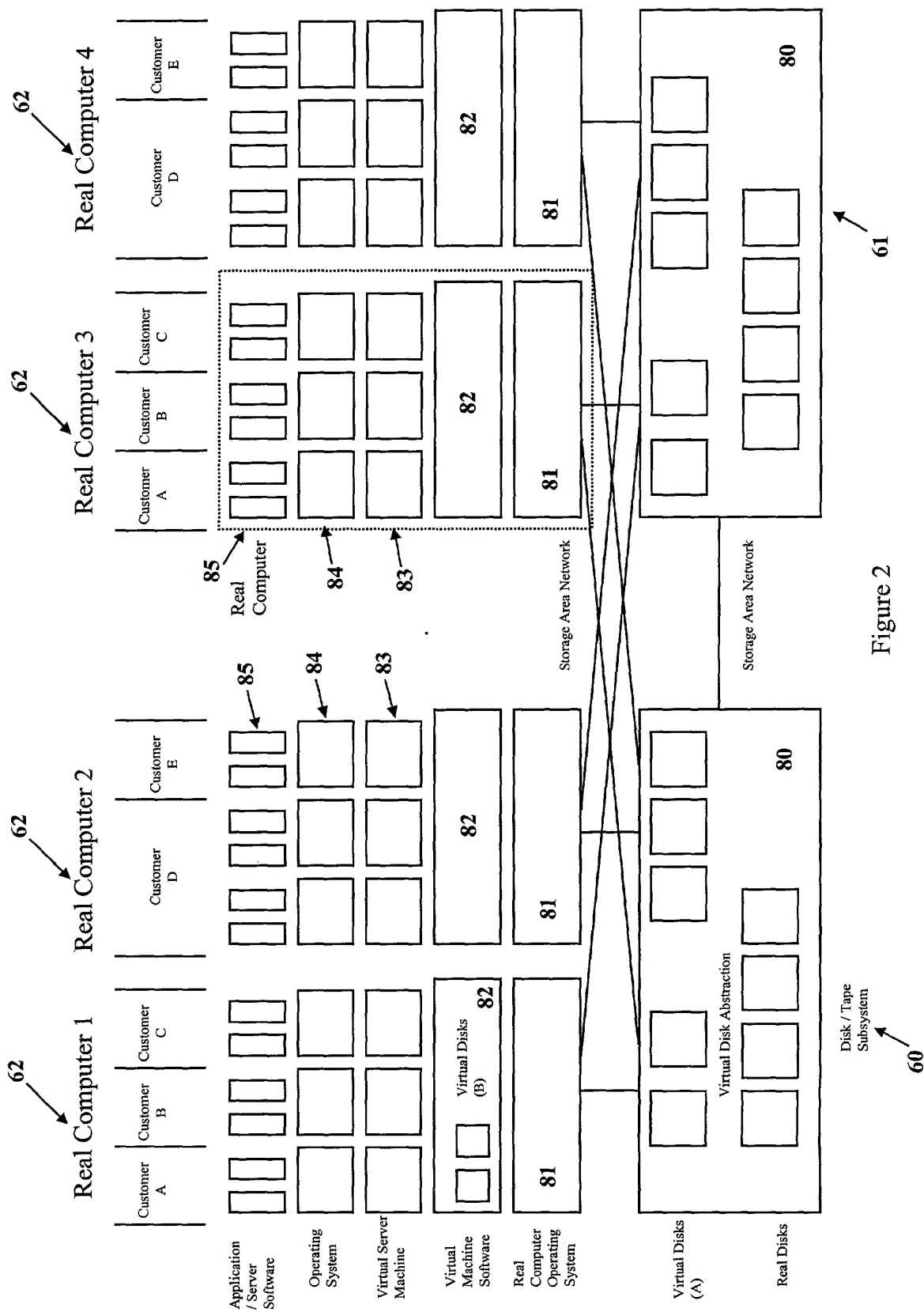
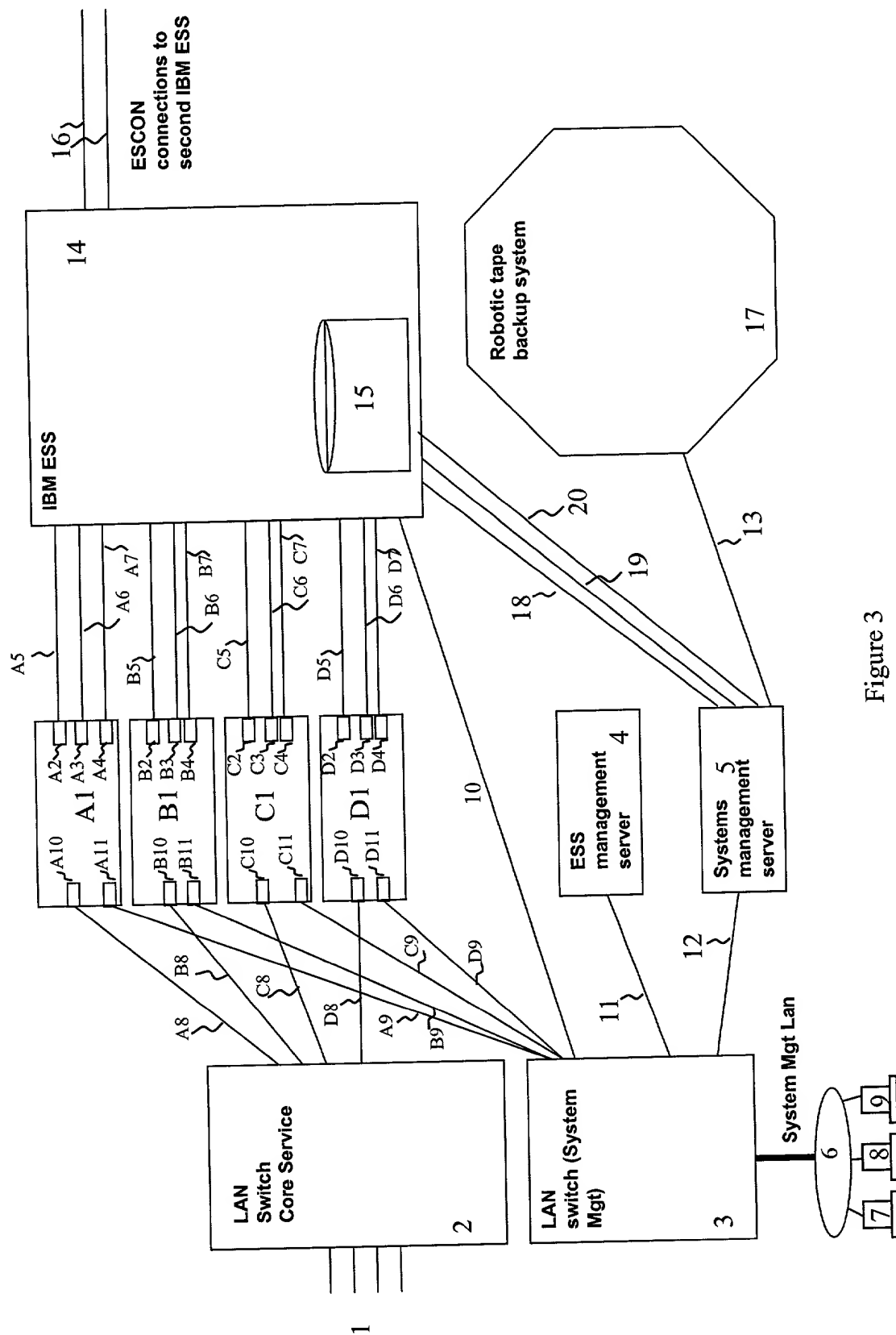


Figure 2



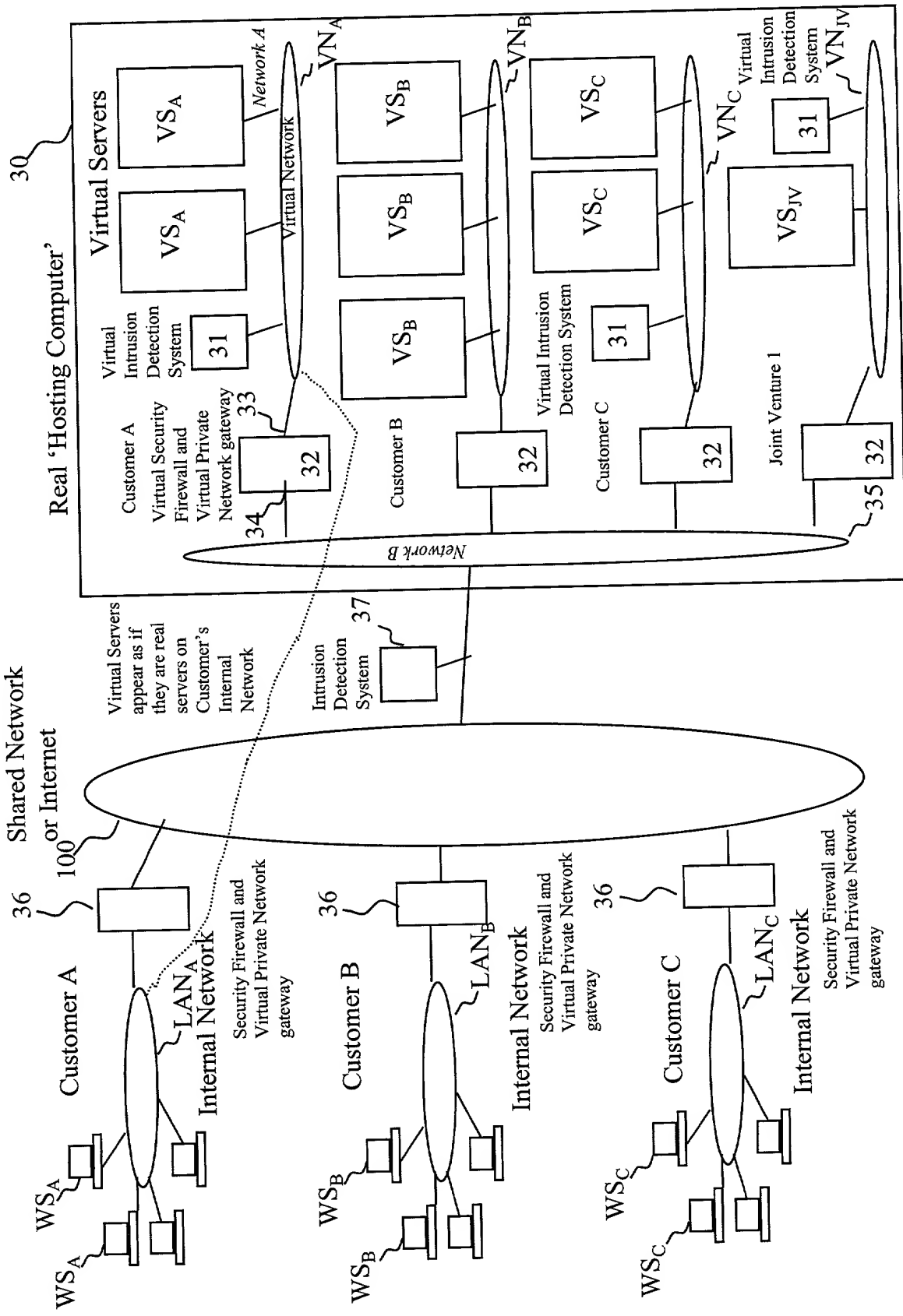


Figure 4

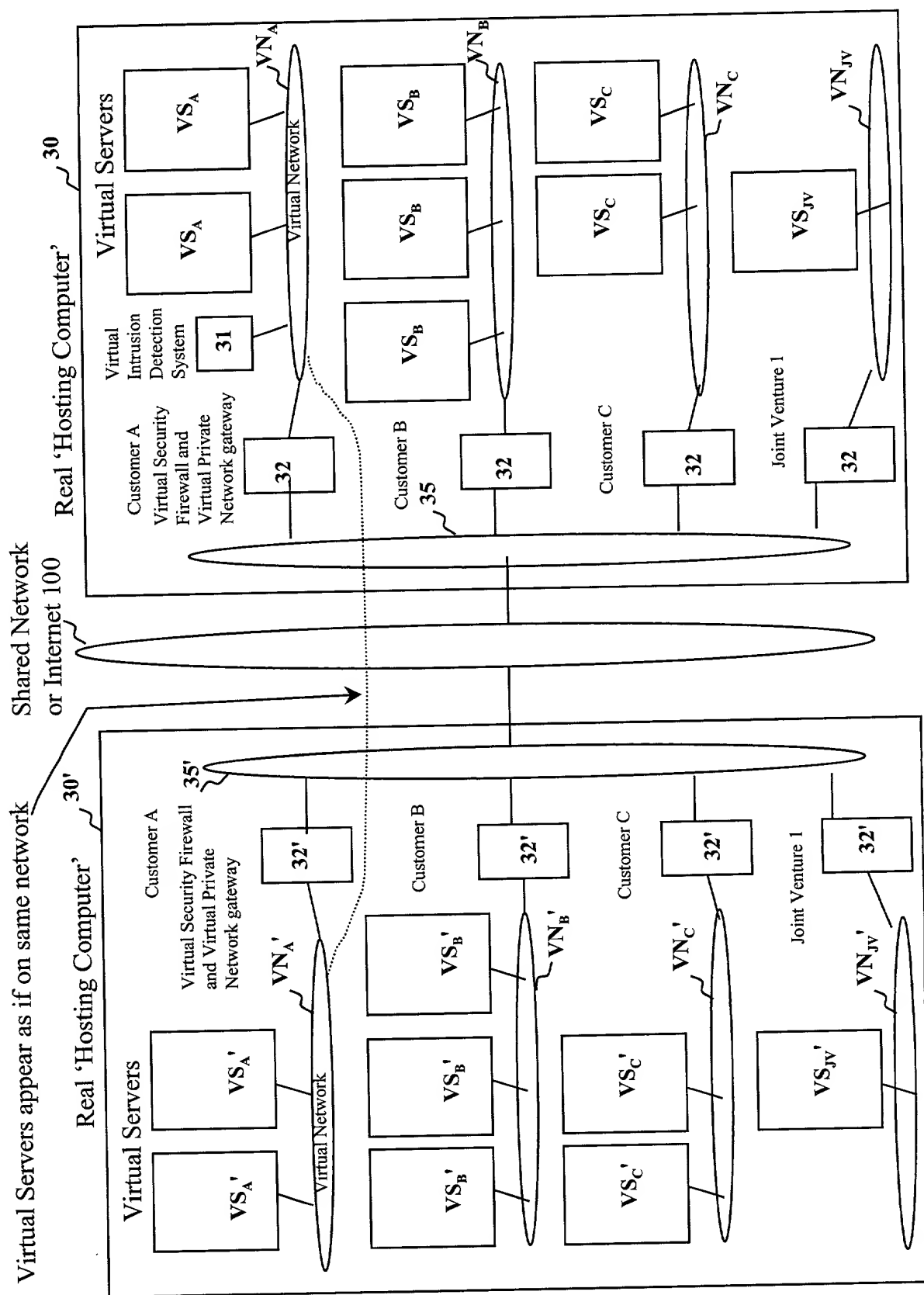


Figure 5

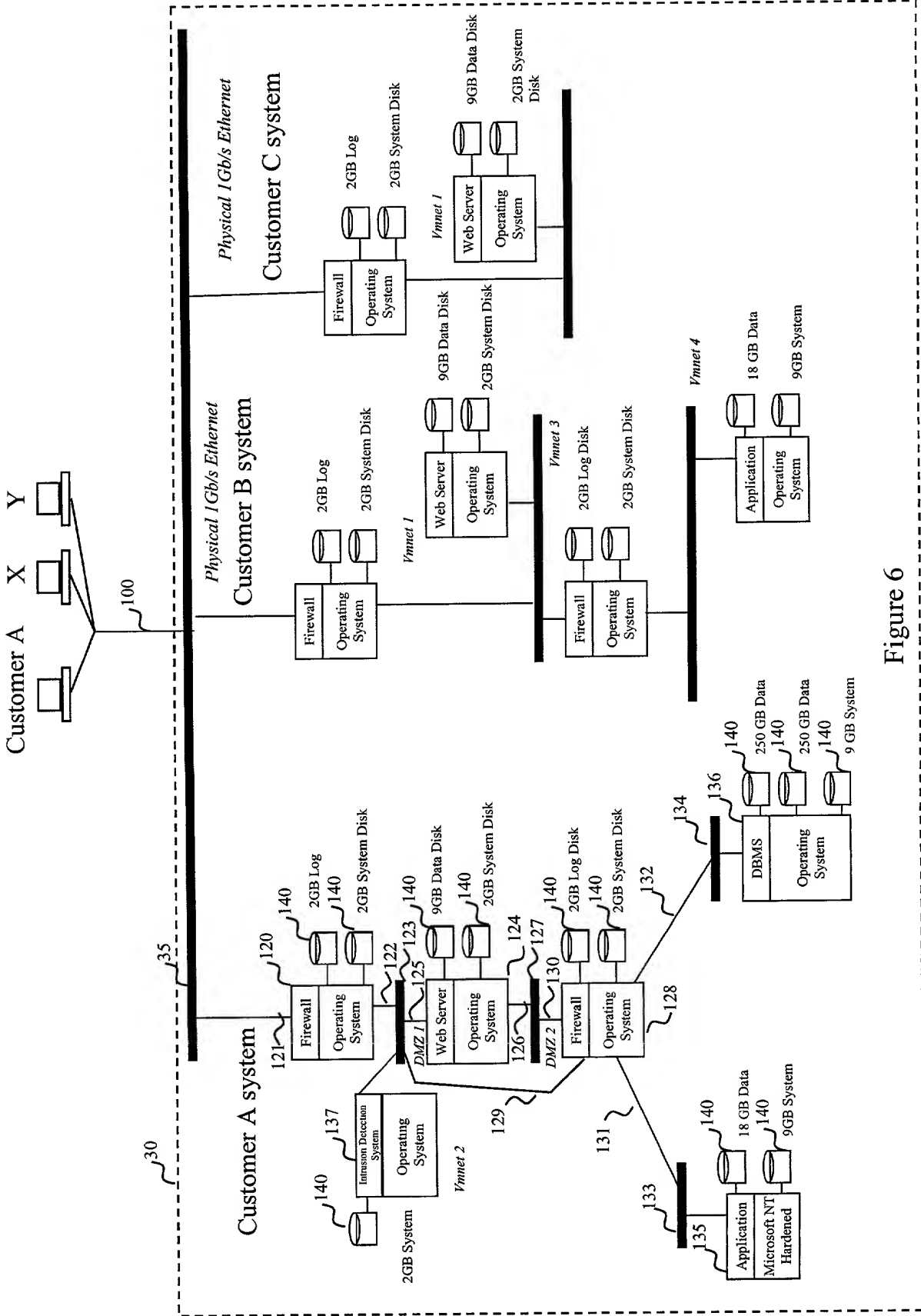


Figure 6